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K.K. Parhi, VLSI Digital  
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Systems: Design and  
Implementation, John  
Wiley, 1999

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instructions running on an arithmetical processing device such as a Microprocessor or Digital Signal Processing (DSP) chip, or as a set of logic operations implemented in a field programmable gate array (FPGA) or in Very Large Scale Integrated Circuit (VLSI).

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## **1.1 Introduction - VLSI Digital Signal Processing Systems**

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He joined IIT, Kharagpur as a faculty member in 1994, where he currently holds the position of a professor in Electronics and Electrical Communication Engg. The teaching and research interests of Prof. Chakraborty are in Digital and Adaptive Signal Processing, VLSI Signal Processing, Linear Algebra and Compressive Sensing.

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