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The 8085 and Its Busses • The 8085 is an 8-bit general purpose microprocessor that can address 64K Byte of memory. • It has 40 pins and uses +5V for power. It can run at a maximum frequency of 3 MHz. – The pins on

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the chip can be
grouped into 6 groups:

- Address Bus.

8085 microproceesor ppt - SlideShare

The Intel 8089
input/output
coprocessor was
available for use with
the 8086/8088 central
processor. It was
announced on May
1979, but the price was
not available at that
time.

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Intel 8089 - Wikipedia

The 8085
Microprocessor
Architecture * * The
8085 and Its Busses
The 8085 is an 8-bit
general purpose
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of memory. - A free
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PPT - The 8085 Microprocessor Architecture PowerPoint ...

The INTEL 8085 microprocessor is a second generation microprocessor and is an eight-bit processor designed in the year of 1976 with the NMOS technology with a 40 pin DIP, approximately consisting 6500 transistors having a power supply of 5V.

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The Intel 8086
microprocessor is the
foremost
microprocessor
introduced in the year
of 1978 by the Intel
Company and it is a 40
pin IC chip. It uses 20

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line address bus and
16 line data bus. The
16 bit lower order
address bus lines are
multiplexed with
complete 16-bit data
lines.

8086 Microprocessors Seminar PPT with pdf report

4 Introduction

Microprocessor is
fabricate a CPU on a
single chip Both metal-
oxide semiconductor

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(MOS) and bipolar technologies are used in the fabrication process. Along with the microprocessor chip, appropriate memory and I/O chips can be used to design a microcomputer. Microcontrollers include a microcomputer, timers, and A/D (analog-to-digital) and D/A (digital to analog) converters, all ...

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INTRODUCTION TO MICROPROCESSORS

- ppt download

8085 microprocessor
architecture ppt 1.

Chapter 2

Microprocessor Archite
ctureMicroprocessors &
Interfacing Dr. Bassel
Soudan 1 2.

Microprocessor
Architecture • The
microprocessor can be
programmed to
perform functions on
given data by writing
specific instructions

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Microprocessor
into its memory. ...
Ppt

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86 Architecture

Programming

Interfacing

MICROCOMPUTER 8086

BUSES ARCHITECTURE

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CONNECTIONS, TIMING

& TROUBLESHOOTING

TIMING SEQUENCE

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...

The microprocessor
contains millions of tiny
components like
transistors, registers,
and diodes that work
together. Block

Diagram of a

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Microcomputer. A microprocessor consists of an ALU, control unit and register array. Where ALU performs arithmetic and logical operations on the data received from an input device or memory. Control unit ...

**What is
Microprocessor:
Block Diagram,
Evolution, Working**

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The 8085 Machine Language • The 8085 (from Intel) is an 8-bit microprocessor. – The 8085 uses a total of 246 bit patterns to form its instruction set. – These 246 patterns represent only 74 instructions. • The reason for the difference is that some (actually most) instructions have multiple different formats.

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Basic Concepts of Microprocessors

A 8085 microprocessor, is a second generation 8-bit microprocessor and is the base for studying and using all the microprocessor available in the market. Registers in 8085: (a) General Purpose Registers - The 8085 has six general-purpose registers to store 8-bit data; these are identified as- B, C, D, E,

Where To Download 8089 Microprocessor H, and L. Ppt

Registers of 8085 microprocessor - GeeksforGeeks

8085 is pronounced as "eighty-eighty-five" microprocessor. It is an 8-bit microprocessor designed by Intel in 1977 using NMOS technology. It is an 8-bit register used to perform arithmetic, logical, I/O & LOAD/STORE operations. It is

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connected to internal data bus & ALU. As the name suggests, it ...

Microprocessor - 8085 Architecture - Tutorialspoint

8085 microprocessor
Architecture Explained
every aspects of 8085
processor Included PPT
File ----- [http://rg.to/file/
94946007b4cc133065
4...](http://rg.to/file/94946007b4cc1330654...)

8085 Microprocessor Architecture

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EE8551 and EE6502 for
EEE Dept UNIT 1 8085
PROCESSOR =>

Hardware Architecture
of 8085 Microprocessor

=> Pin Diagram of

8085 Microprocessor

=> Memory Interfacing
of 8085 Microprocessor

=> Timing Diagram
and machine cycles of
8085 Microprocessor

=> Interrupts and
Types of Interrupts in
8085 Microprocessor

=> Hardware

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Architecture of 8086

Microprocessor =>

8086-Minimum mode
of operation

Microprocessors and Microcontrollers - EE8551, EC8691 ...

1. Internal architecture of 8085 microprocessor
2. 8085 system bus
3. 8085 pin description.
4. 8085 functional description.
5. Programming model of 8085 microprocessor
6. Addressing modes.
- 7.

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Instruction set
classification. 8.
Instruction format. 9.
Sample programs.

Tutorial On Introduction to 8085 Architecture and Programming

The following image depicts the pin diagram of 8085 Microprocessor – The pins of a 8085 microprocessor can be classified into seven groups – Address bus.

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A15-A8, it carries the most significant 8-bits of memory/IO address. Data bus. AD7-AD0, it carries the least significant 8-bit address and data bus. Control and status signals

Microprocessor - 8085 Pin Configuration - Tutorialspoint

Pointer And Index
Registers: Pointer And
Index Registers used to

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keep offset addresses .
Used in various forms
of memory addressing.
In the case of SP and
BP the default
reference to form a
physical address is the
Stack Segment (SS-will
be discussed under the
BIU) The index
registers (SI & DI) and
the BX generally
default to the Data
segment register (DS).

8086-Microprocessor- Architecture

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- 5.3 Instruction Set of 8085 Microprocessor .
- 5.4 Timing Diagram for 8085 Instructions .
 - 5.4.1 Timing Diagram of MOV reg, M.
 - 5.4.2 Timing Diagram of MOV M, reg.

(PDF) An Introduction to Microprocessor 8085

INTR, RST 7.5, RST 6.5, RST 5.5 . are maskable interrupts in 8085 microprocessor. Non-

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Maskable Interrupts are those which cannot be disabled or ignored by microprocessor.

TRAP is a non-maskable interrupt. It consists of both level as well as edge triggering and is used in critical power failure conditions.

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ecf8427e.

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