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Switched-capacitor  
circuit techniques:

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topologies, analysis  
and mitigation of  
nonideal effects, and  
filter synthesis.  
Overview of CMOS  
samplers, data  
converters, and PLLs.

## **Electrical and Computer Engineering**

Selectable Data Bus  
(Dual CMOS or Single  
Multiplexed CMOS)  
DCLK Output and  
Programmable Data

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### Converters For

Output Timing  
Simplifies High-Speed  
Digital Interface ; Very  
Wide Input Common-  
Mode Voltage Range  
(0.4V to 1.4V) Very  
High Analog Input  
Bandwidth (> 850MHz)  
Single-Ended or  
Differential Analog  
Inputs ; Single-Ended  
or Differential Clock  
Input

**Analog-to-Digital  
Converters - ADCs -  
ADC | Maxim**

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BCD is a family of silicon processes, each of which combines the strengths of three different process technologies onto a single chip: Bipolar for precise analog functions, CMOS (Complementary Metal Oxide Semiconductor) for digital design and DMOS (Double Diffused Metal Oxide Semiconductor) for power and high-voltage

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elements.

Communications

**BCD (Bipolar-CMOS-DMOS) - Key**

**Technology for  
Power ICs ...**

Since 1999 ST has been an industry leader in the design and manufacture of imaging sensors. The current product portfolio spans a wide range of traditional image sensors from entry level VGA to 24 Mpixels and will soon



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offer products with  
100's of millions of  
pixels.

## **CMOS Image Sensors -**

### **STMicroelectronics**

High Speed Converter  
Innovations on 28 nm  
CMOS. Introducing the  
first of a new line of RF  
converters designed  
for GHz bandwidth  
applications such as  
4G/5G multiband  
wireless  
communications base

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Converters For  
stations, multi-  
standard production  
test systems, and  
defense electronics.

**High Speed A/D  
Converters >10  
MSPS | Analog  
Devices**

The JESD204 and the  
JESD204B revision data  
converter serial  
interface standard was  
created through the  
JEDEC committee to  
standardize and reduce  
the number of data

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inputs/outputs between high-speed data converters and other devices, such as FPGAs (field-programmable gate arrays).

### **JESD204 Serial Interface and JEDEC Standard Data Converters**

We note that the overall energy consumption of the complete data-transmission system is dominated by off-chip

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components, including  
CMOS DAC, laser,  
receiver set-up and  
analogue-to-digital  
converters.

### **Integrated lithium niobate electro-optic modulators ...**

Data Converters, High  
Speed Serdes, Clock  
Circuits, Power  
Management We  
develop mixed signal  
IPs for major industry  
verticals in sub-nano  
and finfet technologies

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Testing, Security and  
Platform  
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**Mirafr Technologies**  
**| Top ASIC VLSI SOC**  
**Semiconductor ...**

Data Converters; see  
all analog products;  
Clocks & Timing.  
Application-Specific  
Clocks; Clock  
Distribution; Clock  
Generation; Crystal  
Oscillators; Jitter  
Attenuators with  
Frequency Translation;  
Interface &

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Connectivity. Wireless  
Communications;  
Photocouplers /  
Optocouplers; RS-485,  
RS-422 & RS-232; AS-  
Interface Products; IO-  
Link Line Drivers ...

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center provides  
breakthrough  
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technology, integrated

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circuit technology and other technologies for optical, wireless and satellite network. If you want to learn more, welcome to MACOM Chinese official website.

## **MACOM - Technologies**

The MAX1044 and ICL7660 are monolithic, CMOS switched-capacitor voltage converters that invert, double, divide,

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or multiply a positive input voltage. They are pin compatible with the industry-standard ICL7660 and LTC1044. Operation is guaranteed from 1.5V to 10V with no external diode over the full temperature range. They

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