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latch latch byte byte e
ter er 2 er 3 o inter 0 0
(to GPRs) r0 (to GPRs)
gh byte w byte inter 1
1 (to GPRs) 1 (to GPRs)
1

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are possible. - Method
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(a) is used in 300~500
Output Pin SSE demo
boards - Method (c)
provides some
protection to the
microcontroller. Output
Pin (a) low voltage on
output pin lights LED
300~500 (b) high
voltage on output pin
lights LED 5V

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Instructions that Su
pport Subroutine Call
bsr <sub>: branch
subroutine <sub> is

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specified in relative addressing mode and is in the range of -128 to +127 bytes from the instruction immediately after the bsr <sub>bsr <sub> instruction The return address is pushed into the stack instruction. The return address is ...

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