

Pulsar Engine Diagram

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You can find the C and Verilog sources for the device on GitHub. [Eric] also has a great block diagram and description of how everything works available. So far, the device can handle AM ...

Ice, Ice, Radio Uses FPGA

Electric vehicles are becoming more and more common on the road, but when they're parked in the driveway or garage there are still some kinks to work out when getting them charged up. Sure ...

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